

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named  
Inventor : Michael N. Dillon et al.

Appln. No.:

Filed : Herewith

For : SEMICONDUCTOR INTEGRATED  
CIRCUIT HAVING VOLTAGE-DOWN  
CIRCUIT REGULATOR AND  
CHARGE SHARING

Docket No.: 03-0833/L13.12-0243

Group Art Unit:

Examiner:

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

EXPRESS MAIL NO.: EV194225997US  
DATE OF DEPOSIT: September 26, 2003

Sir:

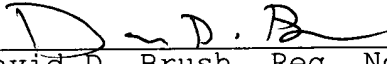
The patents or publications listed on the enclosed PTO Form-1449 are submitted pursuant to 37 C.F.R. § 1.97. Copies of the patents or publications cited are enclosed, except as waived by the Official Gazette notice of August 5, 2003 regarding copies of US Patents and Publications.

The Director is authorized to charge any fee deficiency required by this paper or credit any overpayment to Deposit Account No. 23-1123.

Respectfully submitted,

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FORM PTO-1449	Atty. Docket No.: 03-0833/L13.12-0243	Appl. No.:
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	First Named Inventor:	
	Michael N. Dillon et al.	
	Filing Date	Group Art:
	Herewith	

## U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Date	Name	Class	Sub Class	Filing Date If Appropriate
AA	6,563,343	5/13/03	Patel et al.	326	87	
AB	6,563,192	5/13/03	Corisis et al.	257	532	
AC						
AD						
AE						
AF						
AG						
AH						
AI						
AJ						
AK						

## FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Class	Sub Class	Translation Yes No
AL						
AM						
AN						

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AO	
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AQ	

EXAMINER:

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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.